Ira A. Fulton school of engineering

EE Connections

Electrical Engineering Alumni Newsletter

Fall 2008 /Spring 2009 • Volume 6 • Numbers 1, 2

Newsletter Theme

Each newsletter focuses on one central theme. This one focuses attention on ASU Electrical Engineering graduates who work (or used to work) for Intel. There have been a lot of them. A few are highlighted on these pages and more are listed. We plan to have a second issue about Intel in the Fall. Tell us what we left out and who we left out. Write to us at: atkriege@mainex1.asu.edu

Above: Intel's latest factory - Fab32, Chandler, AZ

We're baaack....

Normally we publish this newsletter once each Fall and Spring semester. Our editor has always been a student majoring in Communication. Because the School of Journalism and Mass Communication moved to Downtown Phoenix, we lost our source of editors and have only now recovered. We are happy to introduce our new student editor, Anne Krieger. She is not a journalism major, but has been expert in assembling the issue. To catch up we are publishing a combined Fall/Spring issue (which is now in your hands).

Vipin Mohan

Design and Technology Solutions

Vipin Mohan received his MSE in August 2008 and is very grateful for the professors with which he worked while at ASU. He would specifically like to thank Dr. Kevin Cao, Dr. Joseph Palais, Dr. Bertan Bakkaloglu, Dr. Dieter Schroder, Dr. David Allee and Dr. Lawrence Clark. He believes that the EE faculty at ASU is among the very best in the country.

His ASU education has helped Mohan with his career. He said that ASU provides students with great opportunities for tie-ups with a myriad of industries. He believes that it is extremely important to leverage those opportunities and encourages students to pursue research activities and internships in order to help EE at ASU to stay ahead of the competition.

Currently, Mohan works for the Design and Technology Solutions group at Intel. He is primarily involved with developing, deploying and supporting RTL level power flows with the goal of enabling CPU teams to identify opportunities and optimize power early in the design cycle.

Michael H. Hiralez

Analog Design

Michael H. Hiralez graduated with a BSE in the fall of 1979. At ASU, he found that professors challenged their students to absorb information and then apply it to the real world situations. Every day, these techniques have helped him solve issues at Intel Corporation.

Hiralez is currently working in the Analog Design Group. His group designs high-speed analog PHY blocks for integrated circuit design. He is responsible for top-level integration of their lower level blocks as well as the integration of blocks into higher-level logic. Hiralez works with mask design technicians, and other circuit designers, to validate and verify various aspects of integrated circuit design. He oversees design rule checking and layout versus schematic checks and the tools associated. Hiralez also supports extraction tools, which measure parasitic resistance and capacitance to ensure circuit design accuracy. In addition, he interfaces with other teams for synthesized digital blocks and their incorporation into a mixed signal world.

Message from the Department Chair

EE alums,

Changes and challenges have been pervasive themes during the past year for our national leadership, the global economy, and ASU's state budget. ASU Engineering is changing in response to both budget challenges and today's engineering technical grand challenges. Deans Meldrum and Johnson have led an effort to embed all existing engineering degree programs and engineering faculty into five large schools. The Electrical Engineering degree programs, faculty, staff and students form the core of the new School of Electrical, Computer and Energy Engineering. The school name reflects the broad interests of our faculty and reinforces the leading role of our degree programs which will retain the Electrical Engineering name. Our research and teaching successes have continued this past year with 102 BSE, 107 MS/MSE and 45 Ph.D. degrees awarded and more than $30,000,000 in research grants awarded. We welcome your feedback and continued support as we move forward to address new changes and challenges.

Stephen M. Phillips
Professor and Chair

Skene Black

Process Engineer

Skene Black graduated in 1995 with a BSE. His experiences at ASU have been beneficial throughout his life. His most memorable professors were Dr. Kozicki, Dr. Higgins, and the late Dr. Demassa. They made course work enjoyable.

His senior design project experience has been particularly useful. He had a strong, well-rounded team and a good mentor, Dr. Akers. ASU's focus on building skills gained from collaboration with other students has helped in his role at Intel Corporation. Those teaching methods have helped him to effectively communicate and work with various groups of people who possess a variety of skill sets.

Black is a process engineer, responsible for the quality of production silicon wafers processed on critical lithography patterning equipment. This involves reviewing statistical data, maintaining specifications for technicians to use, and continuous improvement projects.

Chris Koza

Silicon Component Design / Product Development Engineer

Chris Koza received his BS in 1988 and then his MS in 1991. While Koza was working on his MS, he was involved with the ASU Industrial Fellowship Program. Through this program, he began working at Intel Corporation and gained valuable work experience. ASU strengthened his fundamental understanding of circuits and electronics. He was taught problem-solving techniques, which have been helpful in debugging and solving complex issues.

Currently, Koza is working on chipsets for desktop and mobile PCs, as a silicon component design / product development engineer. He works on implementing and validating design-for-test features, such as memory BIST, TAP controller, JTAG boundary-scan, and general testability support.

David Pivin

Failure Analyst

David Pivin graduated in 1992 with a BSE, in 1995 with a MS, and then he obtained his Ph.D. in 1998.

His advisor, David Ferry, has had a positive influence in both his education and career. According to Pivin, Ferry helped shape him into a persistent professional character. During his graduate years, he learned to respect and admire many ASU professors, especially Dr. Brian Skromme, Dr. Dieter Schroder, and James Mayer. While still in school, Pivin was part of the IEEE Interaction, which provided him with the opportunity to internship at Intel Corporation from 1990-1992.

At Intel, he is responsible for failure analysis studies required to reach and maintain manufacturing yields in the Fabs.

Ivan Reyna

Process Engineer

Ivan Reyna graduated in 1998 with BSEE. Reyna states that the professors at ASU were all very encouraging and inspirational. He wrote that the professors were especially helpful in his upper-level courses, where the coursework became exceedingly tough.

Reyna said the networking opportunities that ASU provided helped him start his professional career. For instance, the contacts he made while at ASU helped him land his current position at Intel.

At Intel, he is a process engineer in the Thin Films Dielectrics group located at F32 in Chandler, AZ. He is responsible for sustaining dielectrics tools processing Intel's latest generation of semiconductor processors.

Sinan Y. Othman

Product Marketing Manager

Dr. Sinan Y. Othman obtained his MS in 1984. He said that ASU provided rigorous academic training, which he later used in his Ph.D. program in EE. He cited Dr. James Cadzow, once a professor at ASU, as particularly influential in both his education and career. At Intel, Othman is a Product Marketing Manager.

Joel Auernheimer

Analog Engineer

In 1999, Joel Auernheimer received his BS and in 2000, he obtained his MSE. He said ASU provided him with a phenomenal foundation on which to build a successful career. He appreciated how his professors were available outside of class; they were always willing to answer questions and discuss opportunities.

Auernheimer is an analog engineer, a position that focuses on power integrity design and analysis for server platforms, including Xeon and Itanium processors. He is a project manager and assists in the development of automated power integrity analysis solutions.

Jill Sciarappo

Director of Strategic Marketing

Having graduated with her BSEE in 1995, Jill Sciarappo is currently the Director of Strategic Marketing in the Intel Corporation's Embedded and Communications Group.

Sciarappo believes that the curriculum and her experiences at ASU were extremely relevant to real-world business issues and have continuously helped her throughout her career. She says that the Cleanroom (ultra-clean environment where microprocessors are made) courses taught by Michael N. Kozicki were especially useful to her because the courses taught her more about the "why" of Intel's cleanroom systems than she had ever realized. The communications and circuits courses are still extremely useful.

She wrote that the courses did not make her an expert but they did plant the seeds for growth of understanding and success.

Umberto Santoni

Marketing Manager

In 1990, Umberto Santoni graduated with a BSEE. The foundational knowledge and skills that he obtained while at ASU were critical in his success at post-graduate programs and in his professional career.

Santoni is currently a Marketing Manager at Intel. He manages the Product Marketing and Applications Engineering Teams for the Embedded Computing Division.

Woody Cohen

Senior Instrumentation and Controls Engineer

Woody Cohen obtained his BSEE in 1988. While at ASU, he encountered excellent professors whose coursework he was able to apply to real-world business applications. He wrote that ASU provided him with the foundation he needed to pursue his career.

Cohen is currently a senior instrumentation and controls engineer at Intel Corporation where he supports building automation systems, which controls wafer manufacturing environments and utilities.

Have we missed you?

Above: Intel's latest factory - Fab32, Chandler, AZ

Intel hires a great number of ASU graduates. If you have ever been employed by Intel Corporation and would like recognition for your work, please read the back of this newsletter.

A Few Intel ASU EE Alumni

Deepak J. Aatresh
Rajul R. Amin
Pankaj Aswal
Joel Auernheimer
Jose A. Avalos
Shahrnaz Azizi
Bradley R. Baker
Julie A. Baker
Ganesh Balamitran
Gary S. Barbari
Bryan D. Boatright
Lori R. Borger
Shannon L. Bouchard
Lawrence J. Bruno
Trung Q. Bui
Gonzalo M. Bustillos
Hugh R. Bynum
Joseph A. Carbonaro
John P. Carrieres
Daniel G. Cartagena
Jeffrey P. Casazza
Bibbin Chacko
Jiang Chen
Muhammad Chughtai
Kevin M. Connolly
Daniel J. Dangelo
Sri R. Darmawaskita
Khosro Darroudi
Robert W. Daywitt
Subhashish Deb
Rosanne D. Der
Abhijit K. Desai
Raquel M. Diaz
Kevin J. Doran
Michael A. Eidson
Mohsen Ekhlassi
Jim E. Evers
Steven B. Felling
Larry C. Ferra
Rich J. Fiutko
Douglas F. Foster
Karen E. Fye
Ramkarthik Ganesan
Nandan U. Gangolli
Vamshi Gannaboina
Fuding Ge
Carl E. Geisert
Salvador Gonzalez
William J. Gross, Jr.
Paul S. Gryskiewicz
Neelima Guntaka
Craig D. Hadley
Siamack Haghighi
Andrew Hall
Dong-Ho Han
William R. Haney, Jr.
Mark A. Harrison
George R. Hayek
Jay S. Heeb
Thomas J. Hernandez
David A. Hinz
Nicholas L. Holmberg
Vijayendra Hoskoti
Michael Lannitti, Jr.
Krishna S. Jaiswal
Heidi M. Johnson
Thomas M. Johnson
Matthew S. Jones
Rajeshkumar Kandaswamy
Wonjae L. Kang
James F. Kapp
Jayashree Kar
Roy Karunakaran
Husnara Khan
Mazhar A. Khan
Pradeep R. Kowkutla
Christopher J. Koza
Gabrielle M. Koza
Ravishankar Kuppuswamy
Tolga Kuru
Vincent T. Kwok
Murtuza A. Lakhani
Sundar Lakshmipathy
William W. Lattin
Thang V. Le
Rodney J. Lenz
Zong-Fu Li
Po-Chou Lin
Paul U. Lind
Mitchell Liswith
Cuong Q. Luu
Anna M. Madrid
Rameshbabu Manickam
Bonnie E. Martin
Michael R. May
Richard J. Meinecke
Chad W. Mercer
Anthony C. Miller
Kalpana Mittal
Chad W. Mizner
Akber Mohammed
Atthar H. Mohammed
Sadiq Mohiuddin
Daniel P. Moore
George A. Morris
Naveen R. Munagala
Keshavram N. Murty
Karthik Murugan
Praveen K. Nadella
N. Gopalan Nair
Ayed R. Naqvi
Balaji Narasimmachari
Raymond Nassim
Ajit R. Nayak
Amy Y. Ni
James L. Noble
James A. Nucci
David J. O'Brien
Carlos A. Ortega
Sinan Y. Othman
Aswin Padmanabhan
Mukund S. Pai
Rajesh S. Pamujula
Vineet Pancholi
Sandip Pandey
Javier Parra
Srirama C. Pedarla
Hsin-Yeh S. Peng
David P. Pivin, Jr.
Bheem R. Patel
Srirama C. Pedarla
Hsin-Yeh S. Peng
David P. Pivin, Jr.
Lesley A. Polka
Chetan Prasad
Noman H. Qadri
Md Quddus
Sukumar Raghuram
Towhid Rahman
Sridhar Rajagopal
Vijay Raman
Manikandan Ramkumar
Nitin Rao
Ivan Reyna
John Rinehimer, Jr.
Gregory Rozzell
David Ryan
Chang-Myung Ryu
Gregory Sabin
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Sherie L. Taylor
Patrick A. Themins
Sanjay K. Thodupunuri
Keith R. Tinsley
Panayiotis A. Tirkas
Mahmoud M. Tomeh
Danny M. Tran
Roy T. Trevino
Yuan-Po Tseng

Keep in touch with EE. Please e-mail your career updates to askee@asu.edu

Electrical Engineering Alumni Newsletter

This publication is written, designed and produced by the Department of Electrical Engineering of the Ira A. Fulton School of Engineering at Arizona State University for distribution to alumni, industry partners and academic friends worldwide.

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Are you a current or former Intel Employee?

We are currently seeking ASU Electrical Engineering graduates that are former or current Intel employees. We are sorry that we have missed a lot of graduates in this issue. For our next issue, we would like to give them recognition for their work at Intel.

If you are an ASU EE graduate that has been or is currently employed by Intel, email Anne Krieger with your name, graduation year and the best way to contact you. Email Anne at: atkriege@mainex1.asu.edu.

We will again highlight graduates and include a list of former and current Intel employees.

Celebrate Your Alumni Status

We invite you to celebrate and honor your alumni status by keeping in-touch with the Electrical Engineering department at ASU.

Check out our webpage for news and information about our faculty, our students, and our programs. We are located at: www.fulton.asu.edu/~eee

Support the success and innovation of the Electrical Engineering department by donating a small gift. Your donation will allow the Electrical Engineering department to continue providing the best possible education and renowned opportunities in research, learning and innovation through cutting edge technology.

To make a donation of any amount:

- Call Charles A. Silver at 480.965.9449
- Go online to www.asufoundation.org and click the gold “Invest in ASU” button. On the next screen, click "Ready To Give." Then select “Engineering, Ira A. Fulton School of” when asked to “Select a college or unit to donate to.” When asked to "Select an associated fund" select “Electrical Engineering.”
- Mail your gift to – ASU Foundation, Gift Processing, PO Box 2260, Tempe, AZ 85280-2260. Check should be payable to “ASU Foundation” with “Electrical Engineering” noted on the memo line.

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